



BRS:

Pending

Active

- L1: (50345) implement\$5 near9 hardware
- L4: (8043) register adj5 file
- L6: (13221) processor near9 configuration
- L7: (20102) develop\$5 near9 software
- L8: (2652) instruction near9 specification
- L9: (145) 11 and 12 and 13 and 14
- L10: (28461) add\$4 near9 instruction
- L11: (2558) implement\$5 near9(pipeline or pipe-line)
- L13: (31) 19 and 110 and 111
- L15: (1857) instruction near5 extens\$5
- L16: (7) 113 and 115
- L17: (6361) schedul\$3 and except\$4 and simulat\$4
- L18: (6) 116 and 117

Failed

DBs: USPAT;US-PGPUB

 Plurals

Default operator: OR

 Highlight all hit terms initially

116 and 117

	U	I	Document ID	Issue Date	Title	Current OR	Current
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6477697 B1	20021105	ADDING COMPLEX INSTRUCTION EXTENSIONS DEFINE	716/18	712/15
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6477683 B1	20021105	Automated processor generation system for de	716/1	716/18
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6240502 B1	20010529	Apparatus for dynamically reconfiguring a pr	712/15	
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6058466 A	20000502	System for allocation of execution resources	712/15	709/1;
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6035374 A	20000307	Method of executing coded instructions in a	711/118	711/13
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5890008 A	19990330	Method for dynamically reconfiguring a proce	712/15	713/10

Ready

NUM